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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

*none*  
*Loke*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-324973 11/08/2002

JAPAN 2003-147447 05/26/2003

JAPAN 2003-324204 09/17/2003

*Loke*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Met after Allowance	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and Acknowledged	Examiner's Signature <i>Loke</i> Initials <i>Loke</i>	JAPAN	56	35	2

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## TITLE

Wiring structure on semiconductor substrate and method of fabricating the same

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
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